

FIG. 1 is a schematic diagram of a device 10.

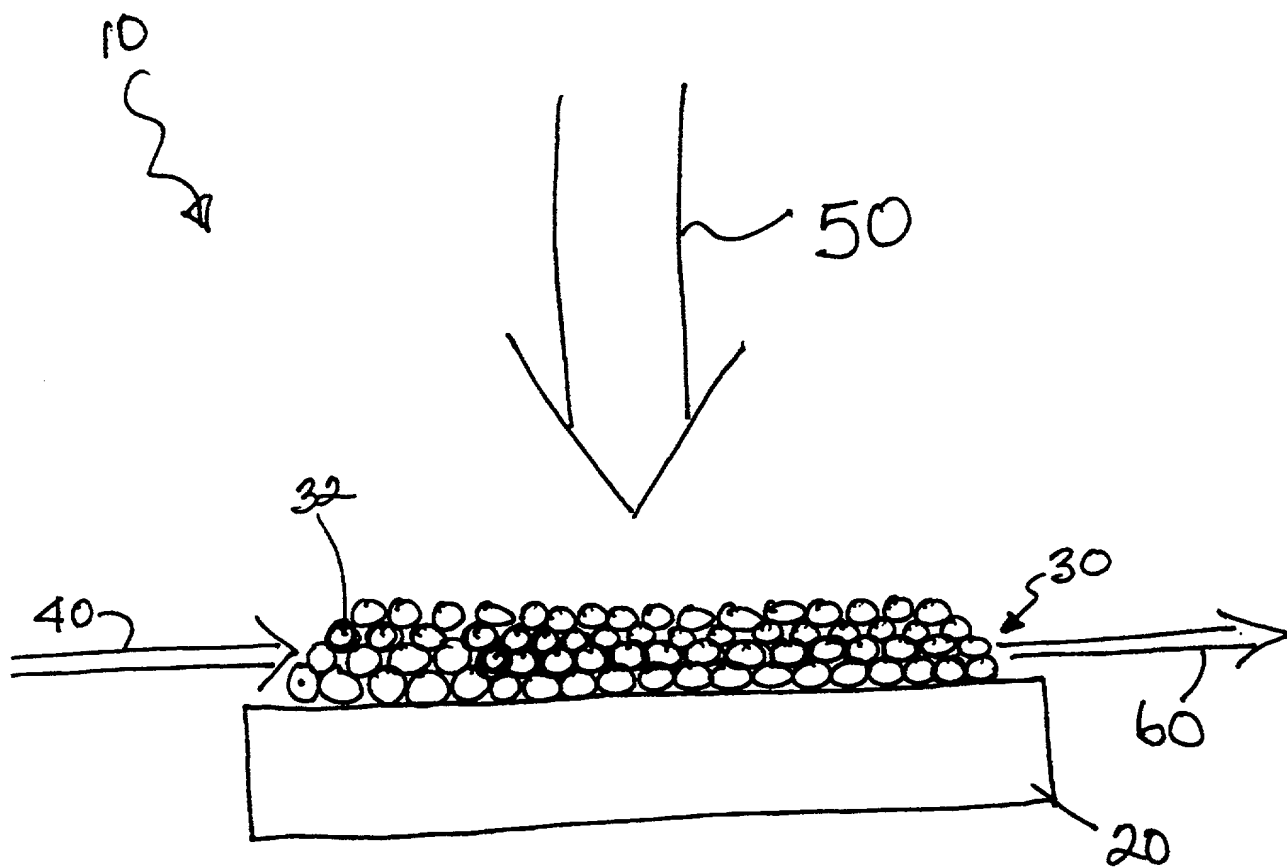


FIG 1

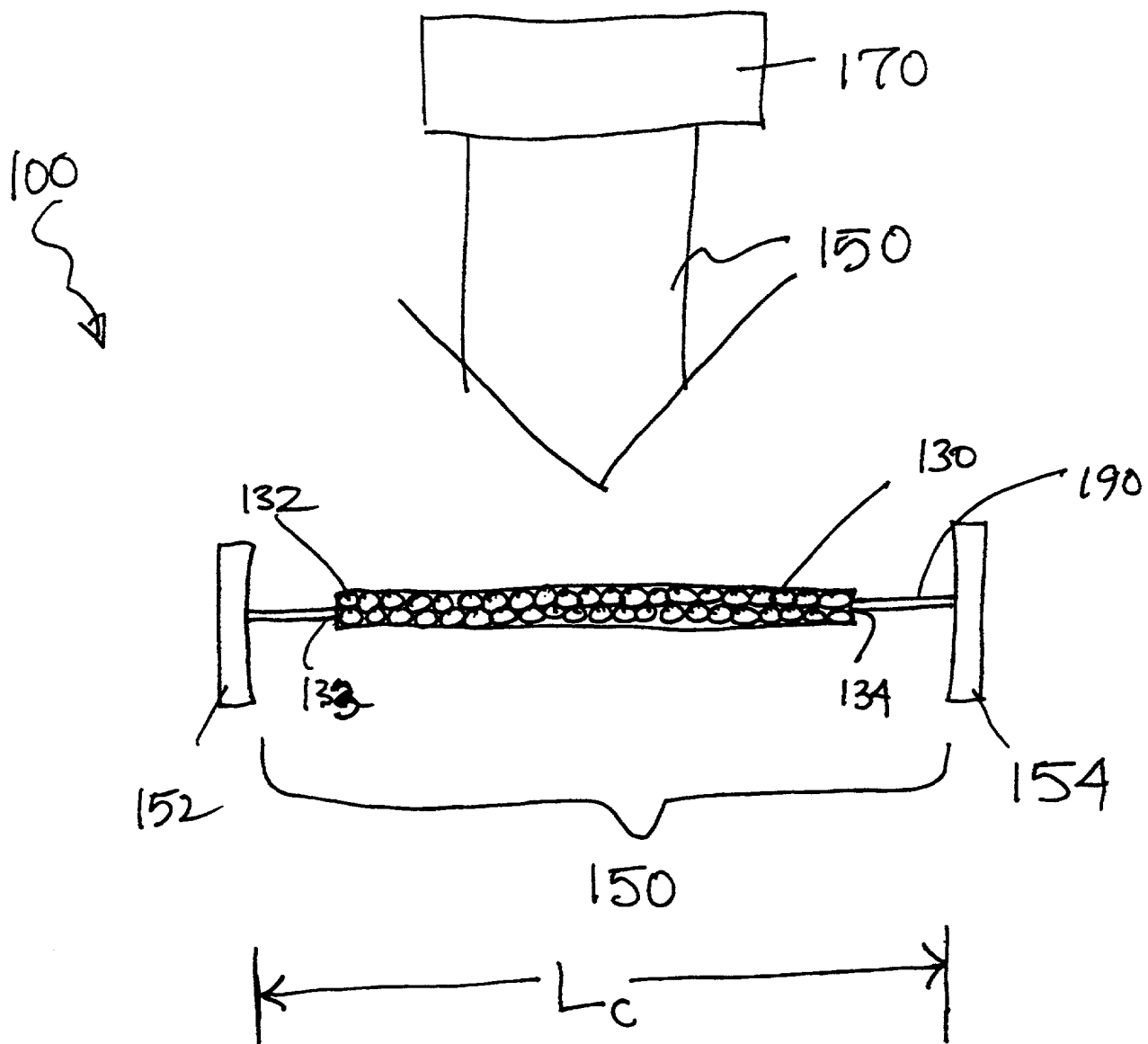


Fig 2

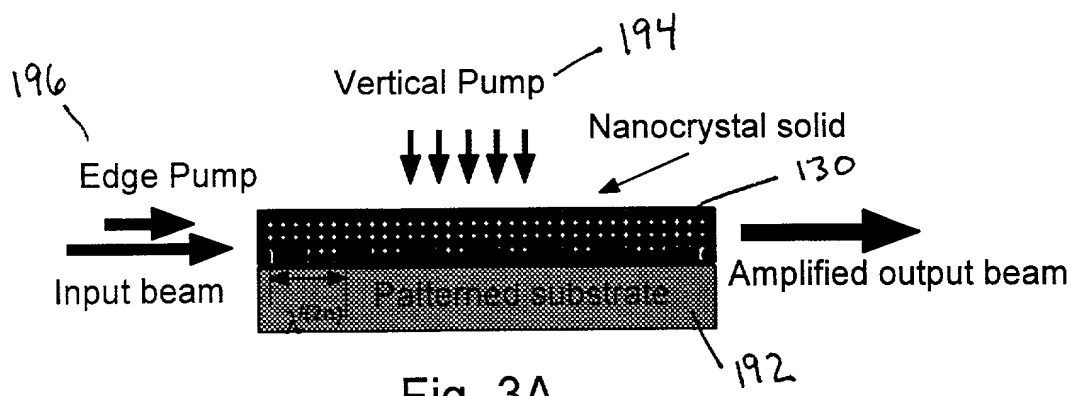


Fig. 3A

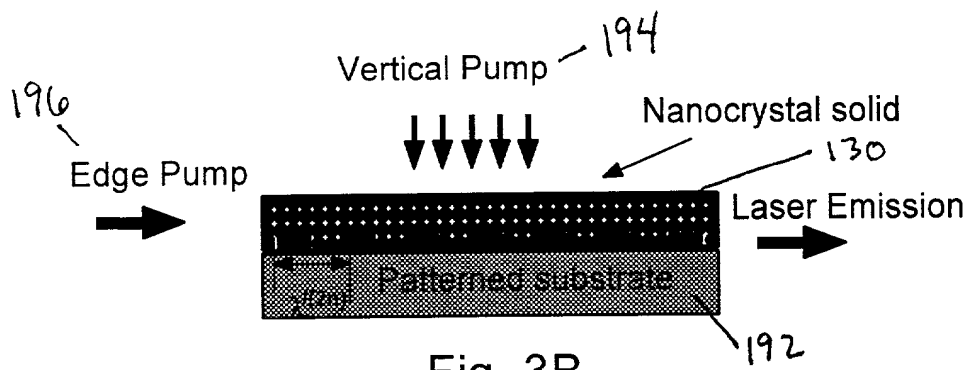


Fig. 3B

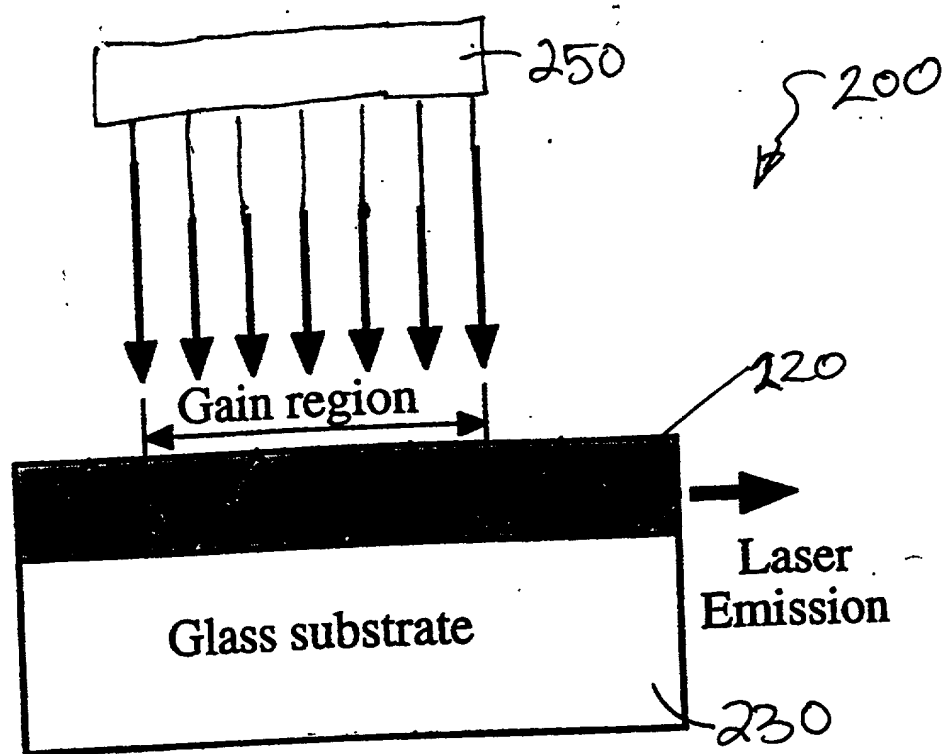


Fig. 4

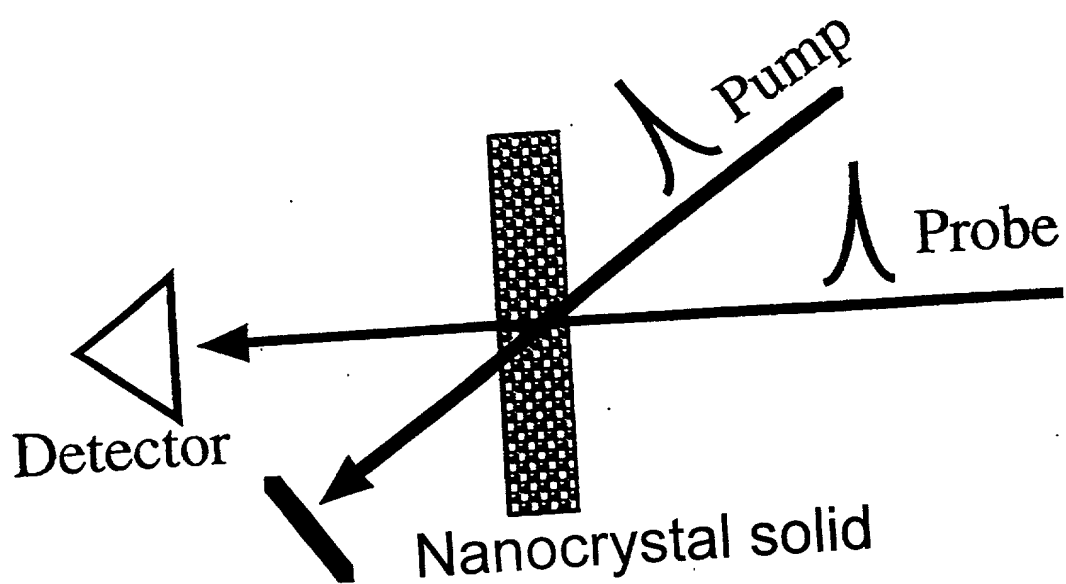
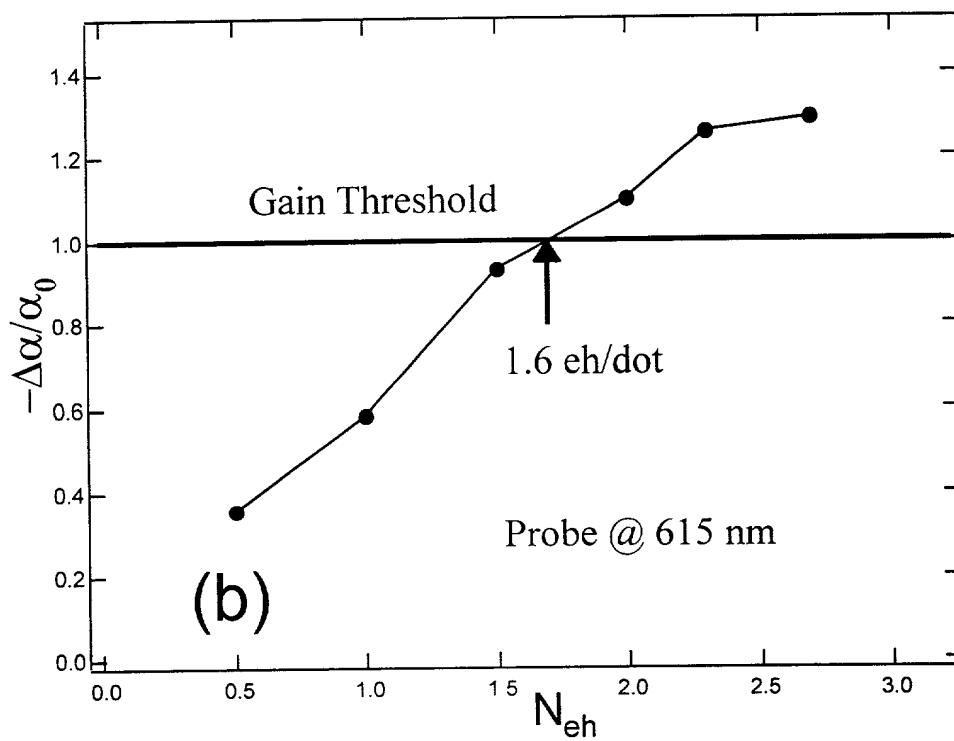
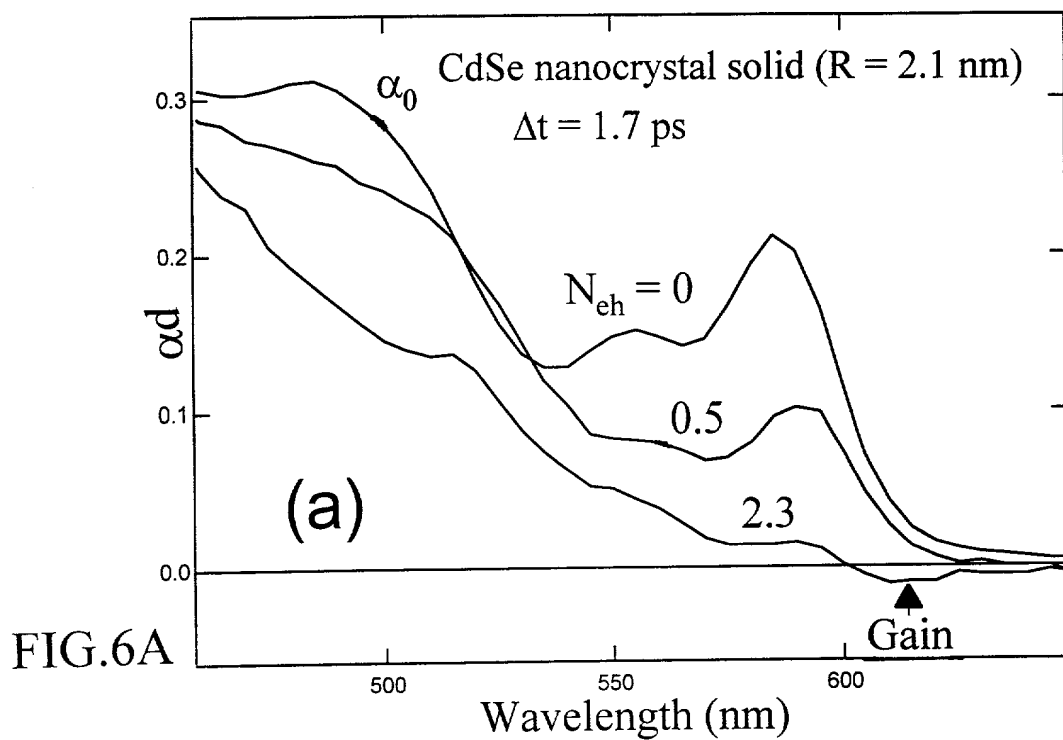


FIG. 5



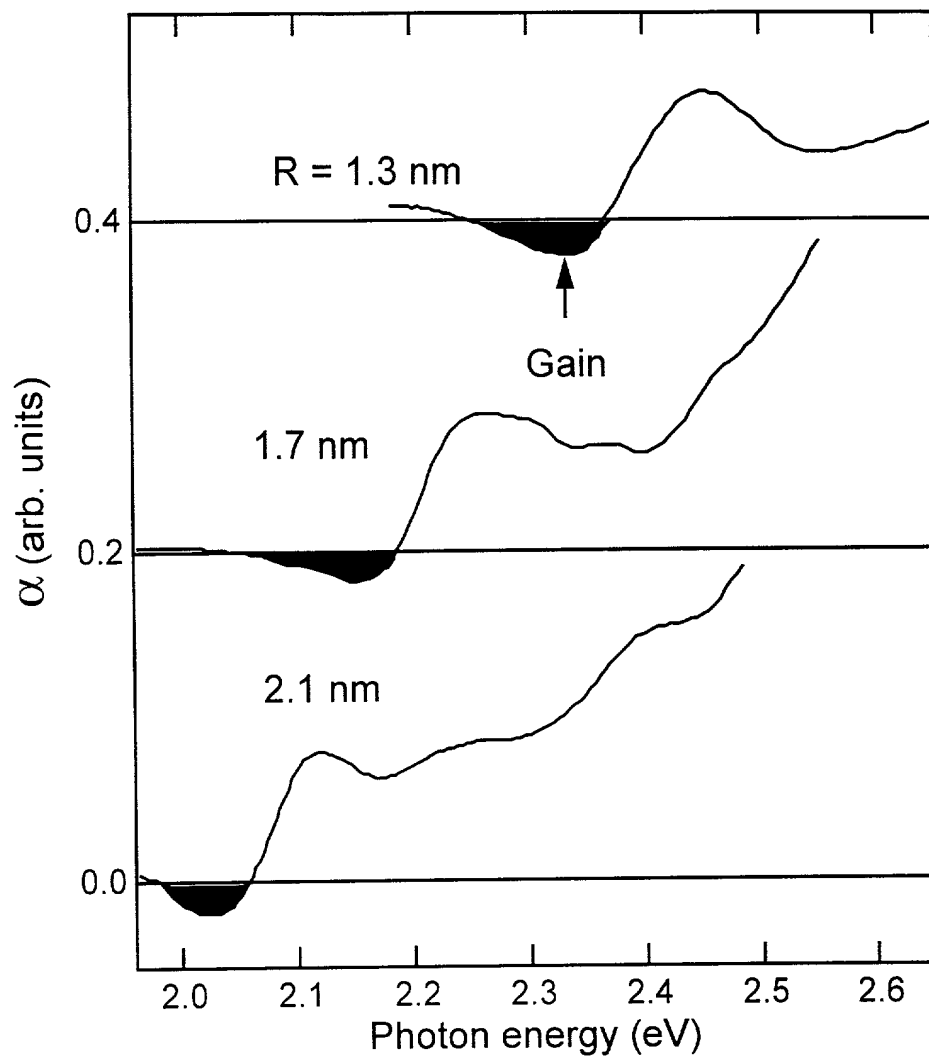


FIG. 7

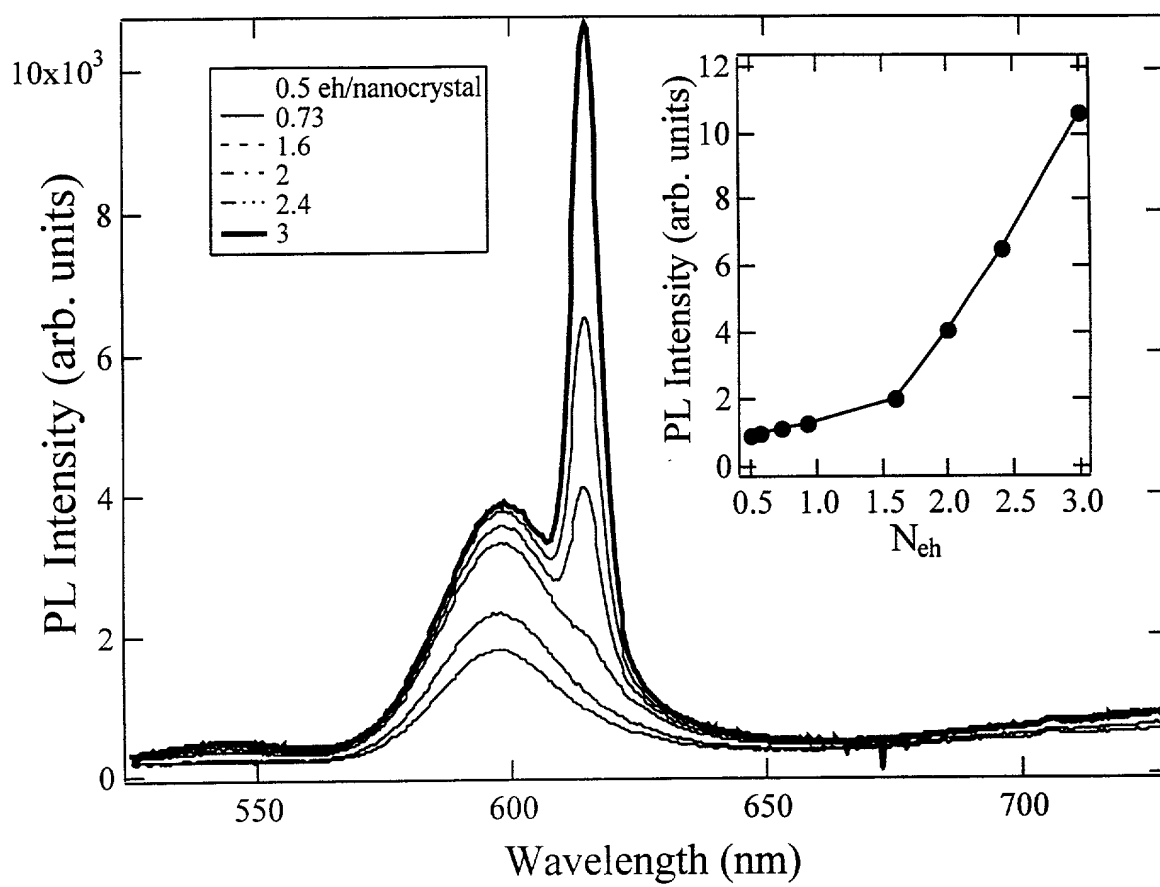


FIG. 8



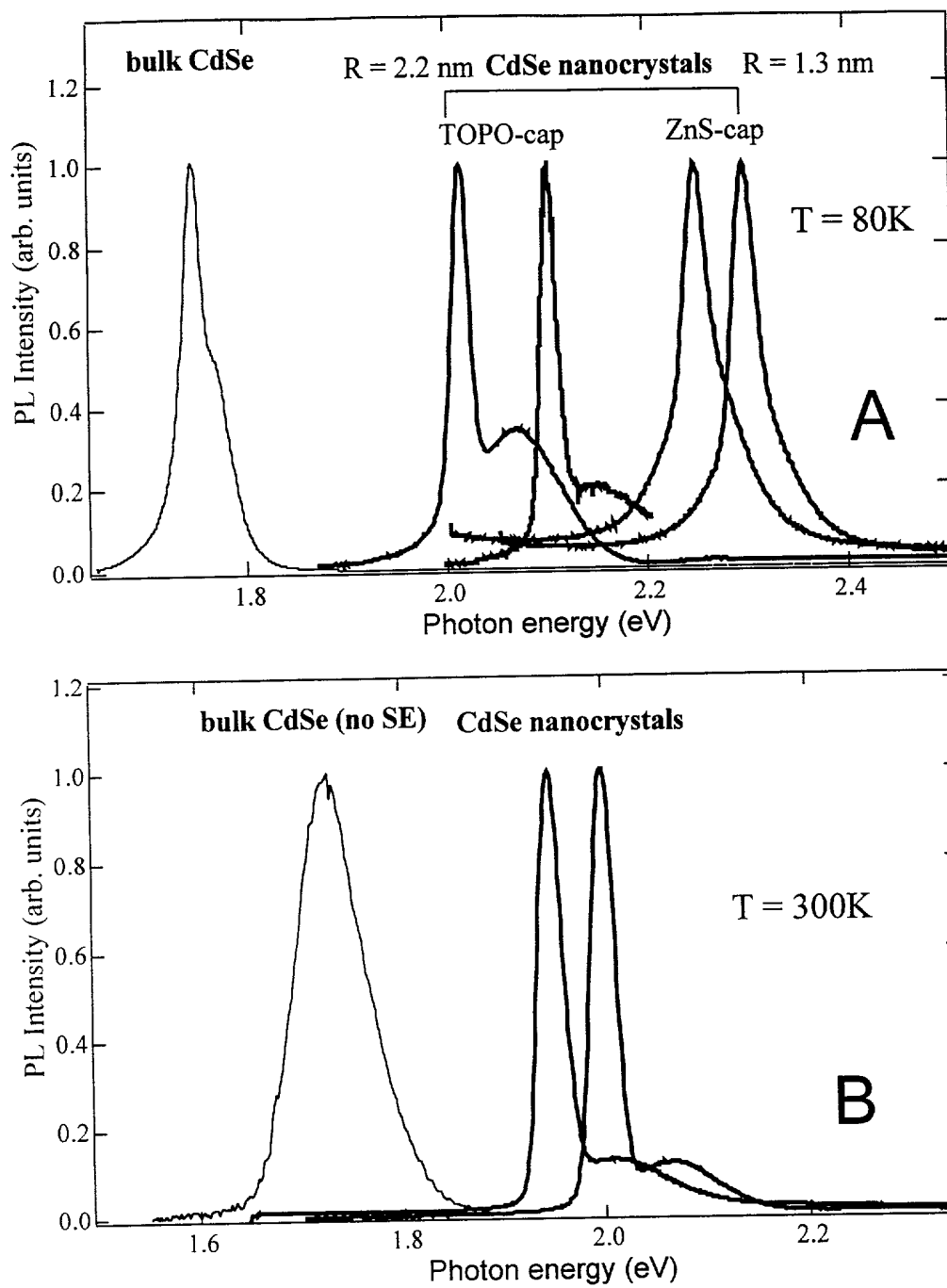


FIG. 9